

informatik-Kolloquium

Der Fachbereich Informatik der Johannes Kepler Universität Linz¹ lädt in Zusammenarbeit mit der Österreichischen Gesellschaft für Informatik (ÖGI) zu folgendem Vortrag ein:

Dr. Gerolf Hoflehner
Intel Microprocessor Research Lab, Santa Clara, USA

HW/SW Co-design: Past, Present and Potential

Wednesday, 25. April 2012, 16:15
JKU, T 038/1 (ground floor TNF-Turm)

Abstract:

With new computer devices such as smartphones and tablets the challenges for modern processor designs keep rising. The past decade has seen design races for high frequencies, multi-cores and low power. This talk looks at future potential for hardware/software co-designed processors and gives a historical perspective of the field.

About the Speaker:

Gerolf Hoflehner is a Research Scientist in the Intel Microprocessor Research Lab, Santa Clara. He currently works on binary translation technology and computer architecture. His background includes extensive work on the Itanium, Mips and IBC 370 compilers. He has a diploma in Mathematics from the Technical University Munich and a PhD in Computer Science from the Johannes Kepler University Linz.

o. Univ.-Prof. Dr. Hanspeter Mössenböck
Institut für Systemsoftware

¹ Der Fachbereich (<http://informatik.jku.at>) besteht aus folgenden Instituten: Anwendungsorientierte Wissensverarbeitung (FAW), Bioinformatik, Computational Perception, Computergrafik, Computer-Architektur, Formale Modelle und Verifikation, Informationsverarbeitung und Mikroprozessortechnik (FIM), Integrierte Schaltungen, „integriert studieren“, Pervasive Computing, Systemsoftware, Systems Engineering und Automation, Telekooperation